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		APPLICANT Charles H. Dennison					
		FILING DATE 12/4/2000		GROUP 2811			
		U.S. PATENT DOCUMENTS					
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
ON	AA	5,668,037	9/97	Prall, et al.			
ON	AB	5,770,496	6/98	Roberts			
ON	AC	5,318,924	6/94	Lin, et al.			
ON	AD	6,479,362B2	11/02	Cunningham			
ON	AE	5,652,464	7/97	Liao, et al.			
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes
	AM						No
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
ON	AR		Stanley Wolf, "Silicon Processing for the VLSI Era: Vol. 2," Lattice Press, Sunset Beach, CA. (1990) pp.273-276.				
ON	AS		J.D. Hayden, F.K. Baker, S.A. Ernst, R.E. Jones, J. Klein, M. Lien, T.F. McNelly, T.C. Mele, H. Mendez, B-Y Nguyen, L.C. Parnillo, W. Paulson, J.R. Pfeister, F. Pintchovski, Y-C See, R. Sivan, B.M. Somero, and E.O. Travis, "A High Performance Half				
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EXAMINER			DATE CONSIDERED		5/6/03		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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	AM						Yes
	AN						No
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
ON	AR			W-H. Chang, B. Davari, MR. Wordeman, Y. Taur, C. Hsu, and M.D. Rodriguez, "A High Performance 0.25- μ m CMOS Technology: Design and Characterization," IEEE Trans. On Electron Devices, Vol. 39, No. 4, (1992), pp. 959-966.			
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